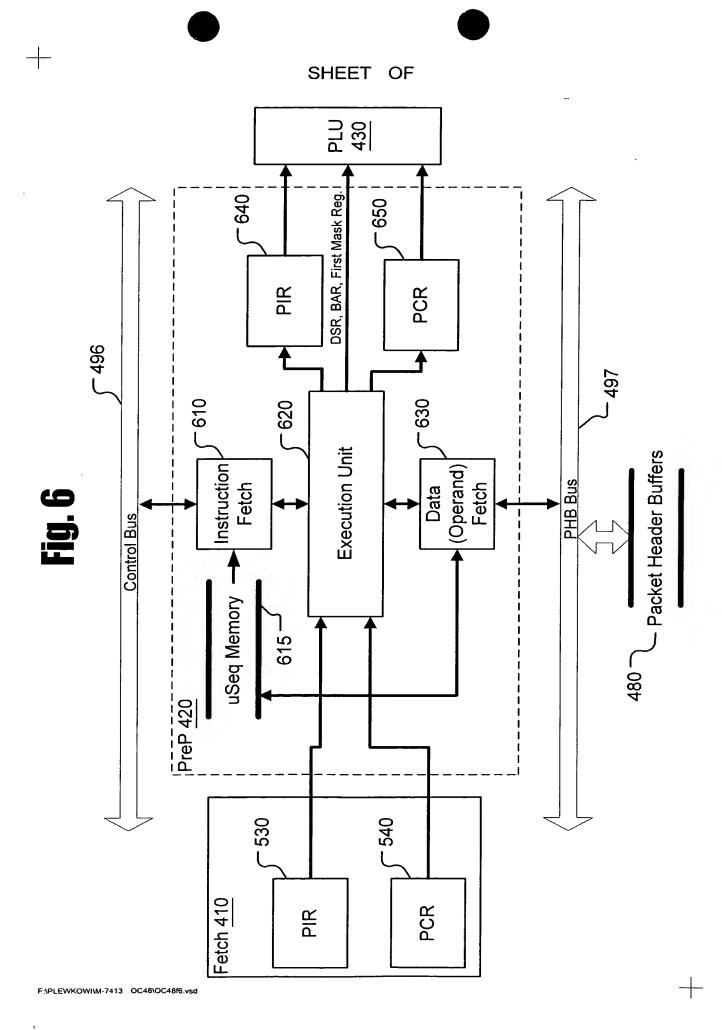
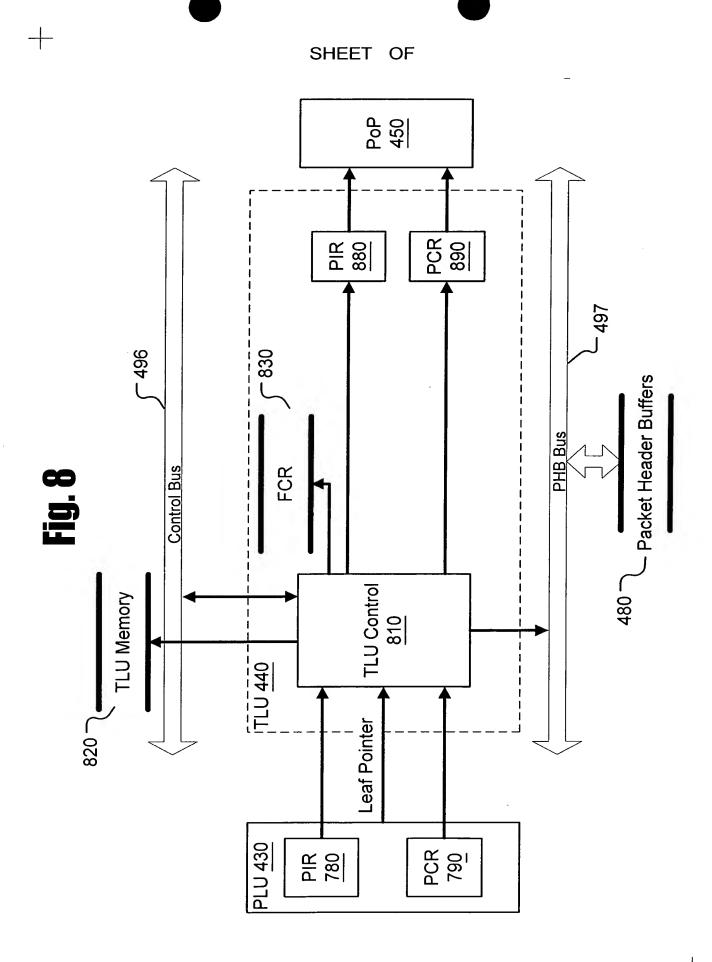


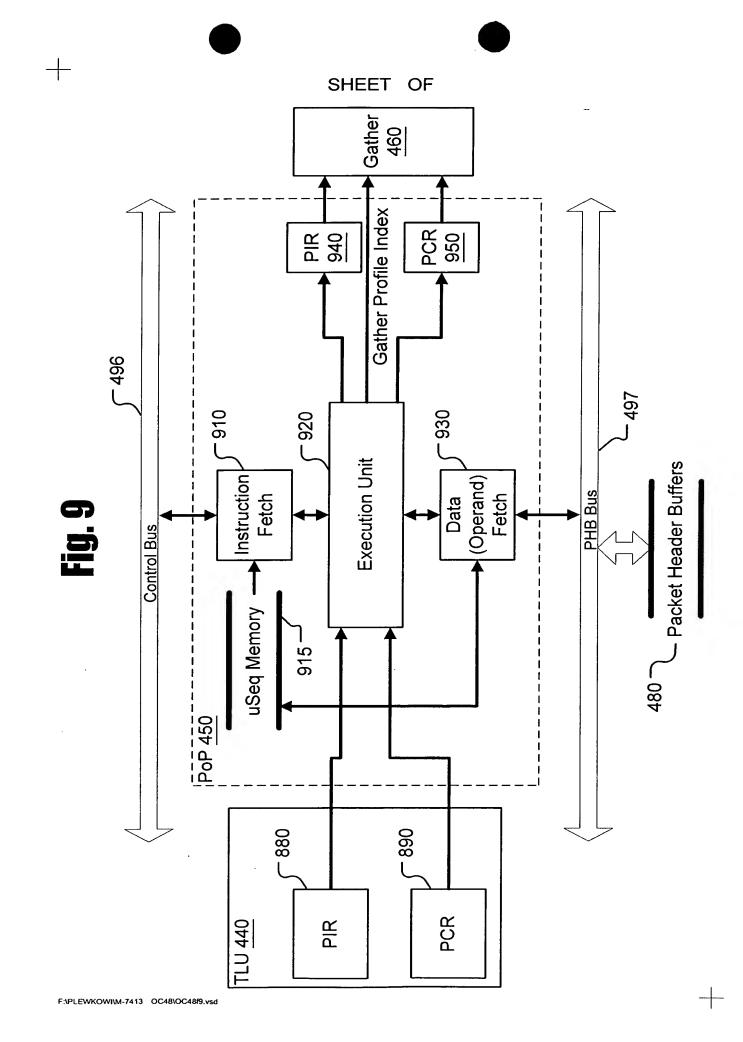
SHEET OF PreP 420 - 540 - 530 **PCR** 몺 - 496 497 510 Header Portions Packet Header Buffers PHB Bus Validator/ Classifier Checksum L2 Header Validator Control Bus 520 Fetch 410 Header Portions Receive FIFO 215.

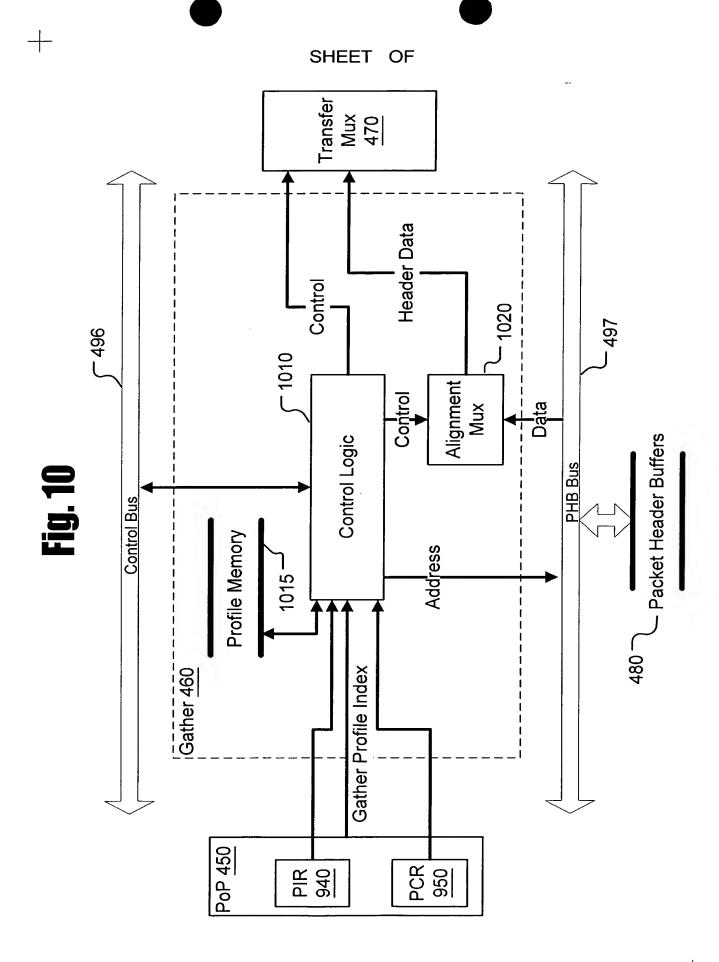
F:\PLEWKOWI\M-7413 OC48\OC48f5.vsd



SHEET OF 7LU 440 PCR 790 PIR 780 Leaf Pointer 496 Control Bus PLU Control PLU Memory PLU 430 DSR, BAR, First Mask Reg. PreP 420 PCR 650 PIR 640







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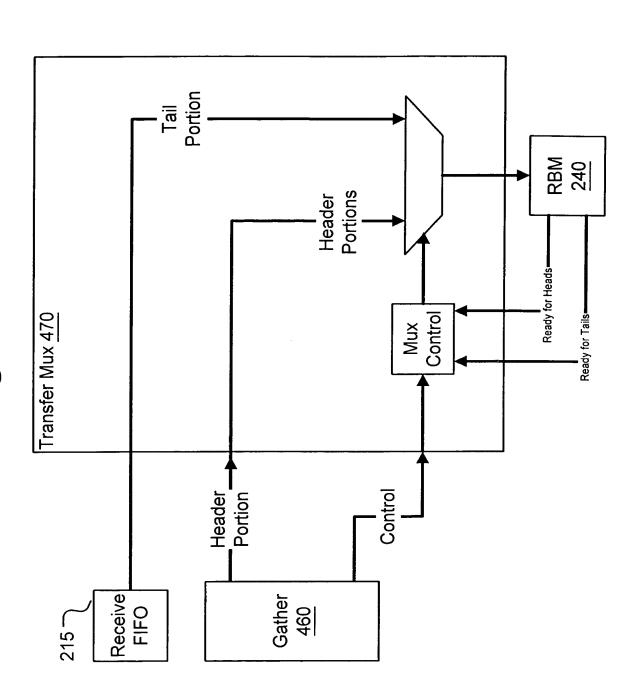
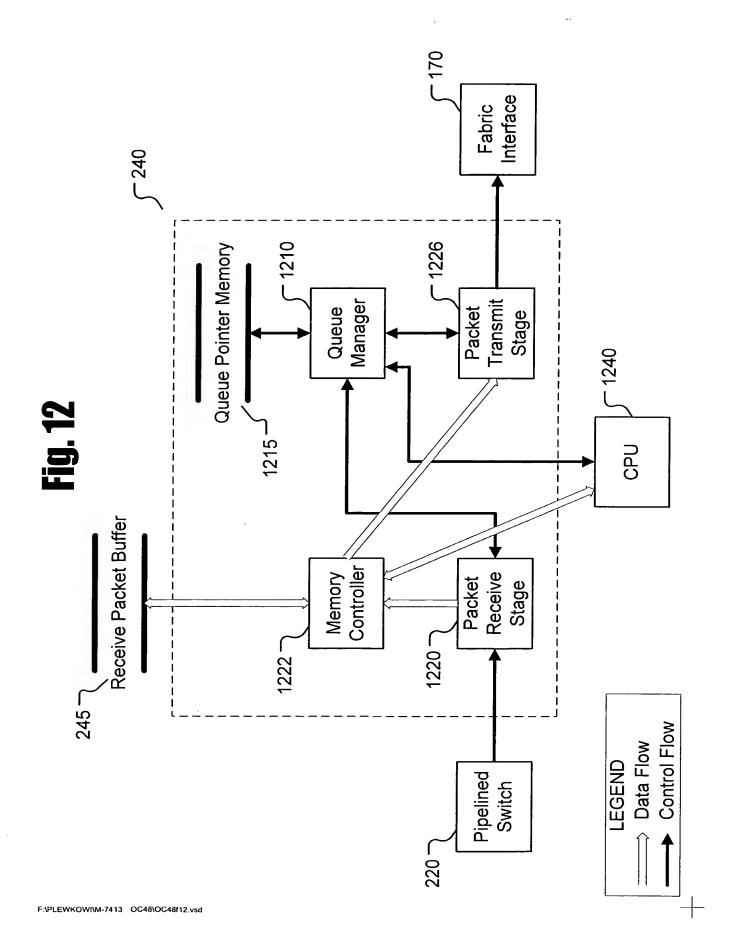
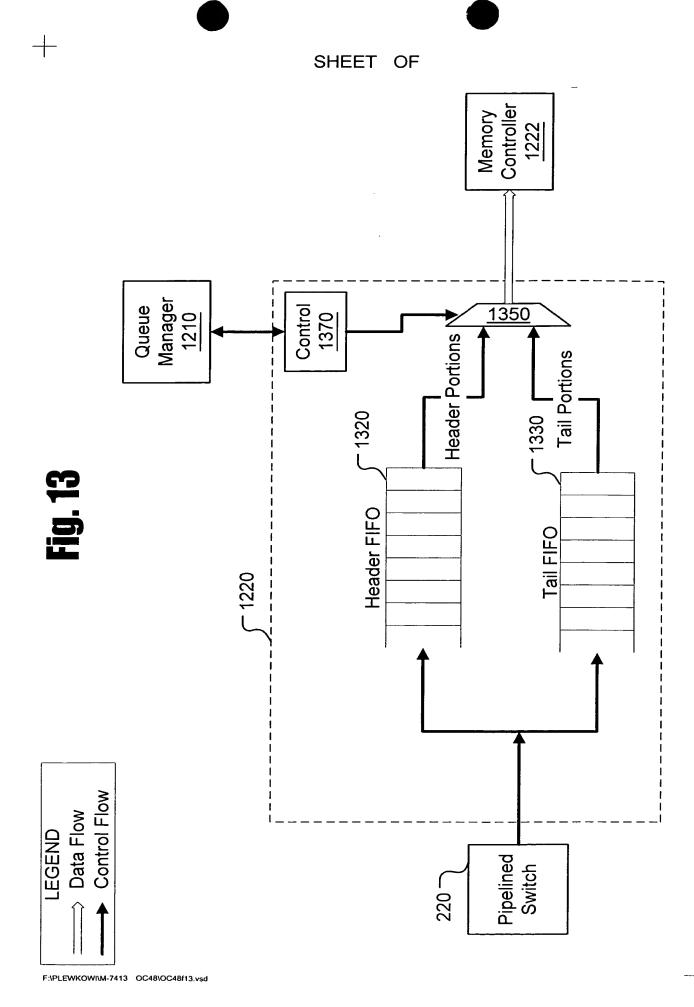


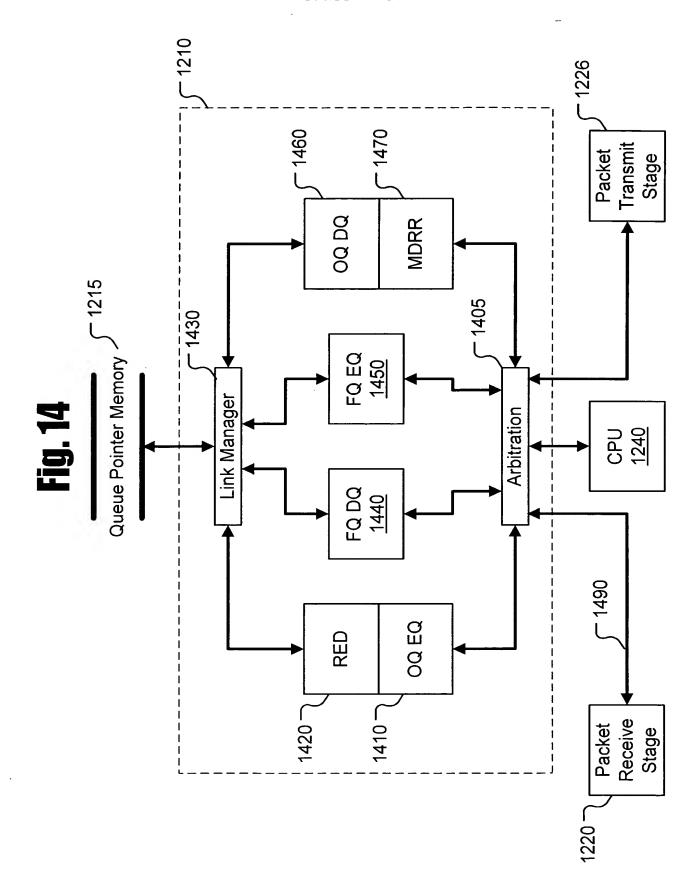
Fig. 11

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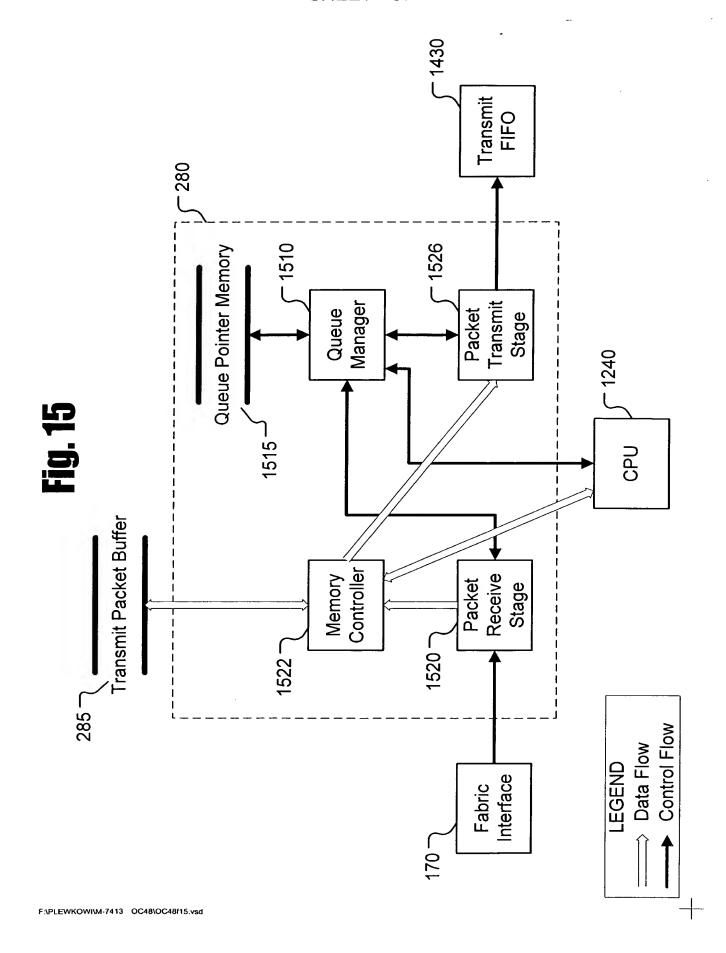




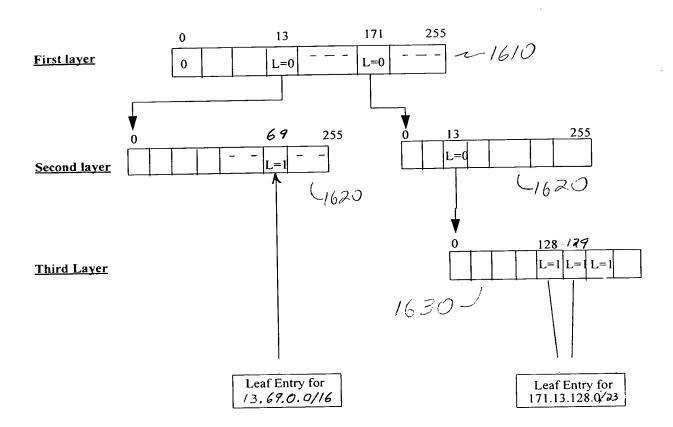
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● #IG. 16 ●

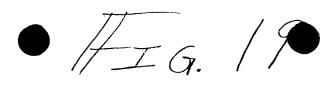


Offset 171 256 **\$**256 13 13 69 0 0 0 L=0; 0xDDDDDD00 L=1; pointer to leaf L=0; 0xCCCCC00 L=0; 0xBBBBB00 Other layer 2 entries **Next Address** shift right 0xFF000000 0x00FF0000 0x000000FF 0x0000FF00

layer 3 starts here 256 layer 2 starts here 0xDDDDDD80 0xDDDDDB1 0xBBBBBB00 0xDDDDD00 0xccccod 0xAAAAAB 0xAAAA00 0xcccc00 Address 256 128 129 0 L=1; Points to leaf L=1; Points to leaf to the same address as both satisfy the /23 Both pointers point Mask select Base Address Register (BAR) Destination Search Reg (DSR) Pointer to top of tree Mask VIII register Mask III register Mask IV register 0x000000FF 0xAAAAA00 Mask II register Mask I register 171.13.128.0

0xAAFFFFC 0xAA000000 data out TAG look up table in the PLU L=1, pointer Ξ = = [=] pointer to TLU 0xAA000004 memory. shift right Mask select=VIII Destination Search Reg (DSR) Base Address Register (BAR) 0xAA0000000 Pointer to top of tree 0x000004XXX Mask VIII register 0xFF000000 Mask III register 0xFFFFF000 0x00FF0000 0x0000FF00 Mask IV register 0x000000FF Mask II register Mask I register

nonce de la second



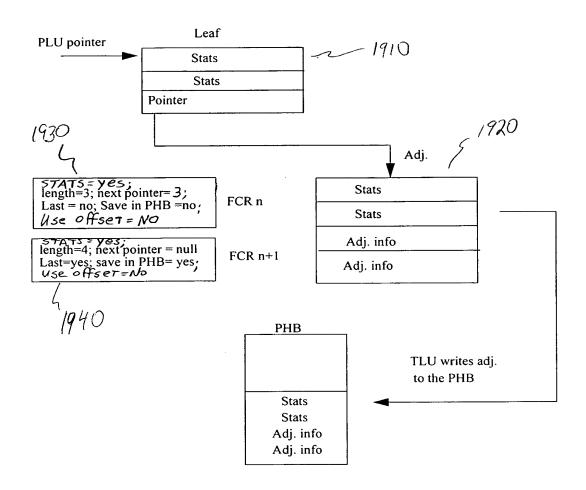


FIG. 20 ●

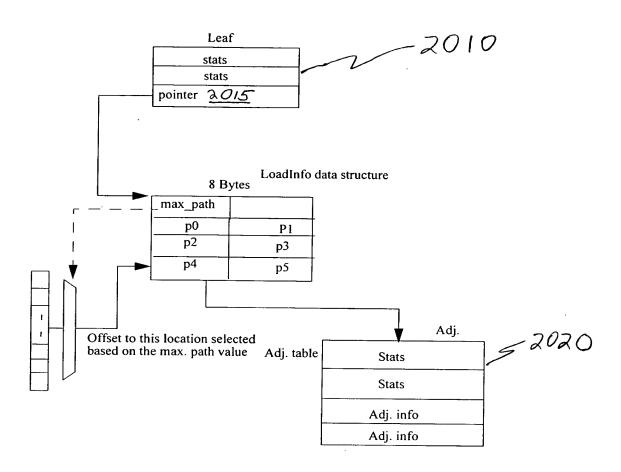
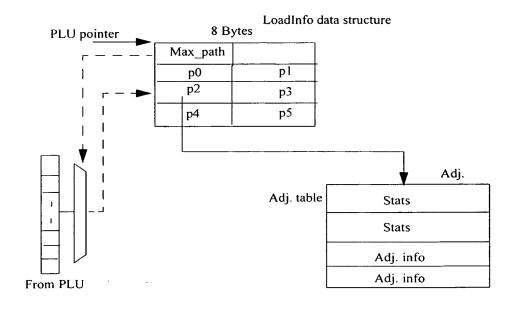
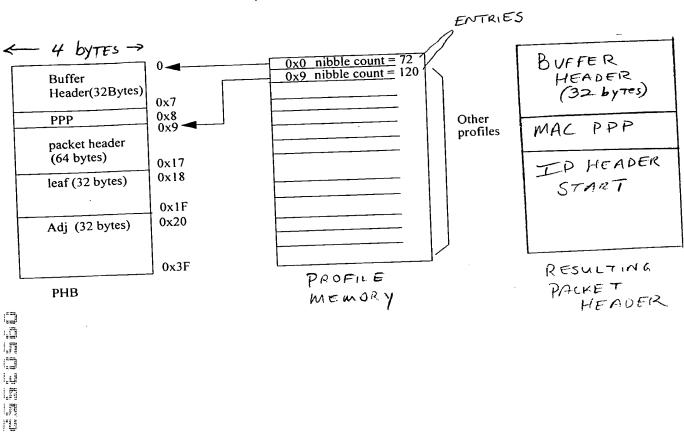
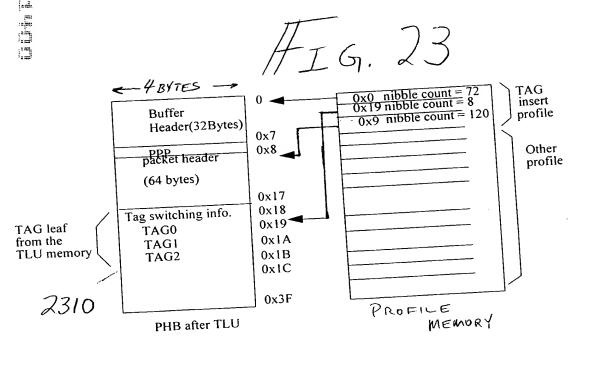


FIG. 21



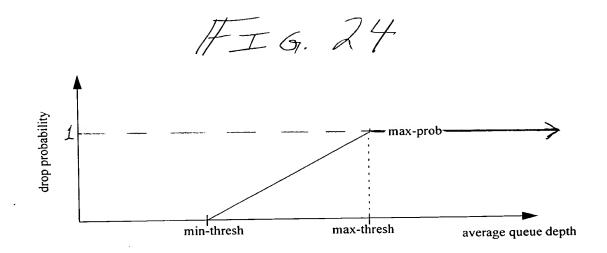
MIG. 22

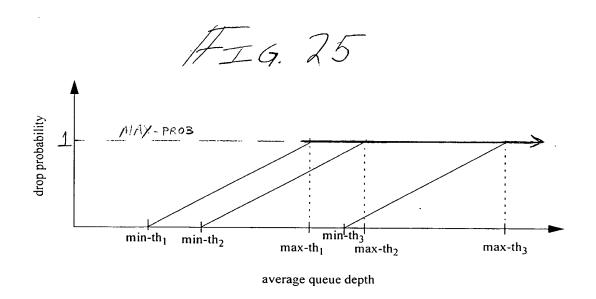




Resulting packet header in RBM:

Buffer Header	
(32 bytes)	
mac PPP	
TAG 0	
IP header start here	
	ļ
[١
	١
	١





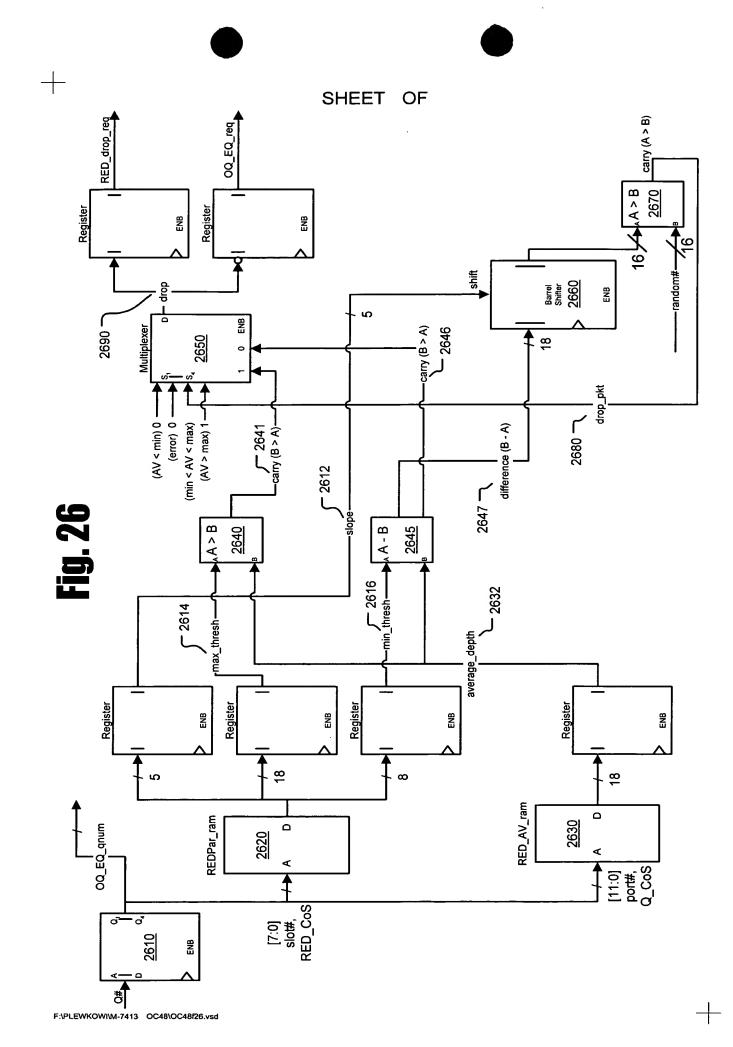
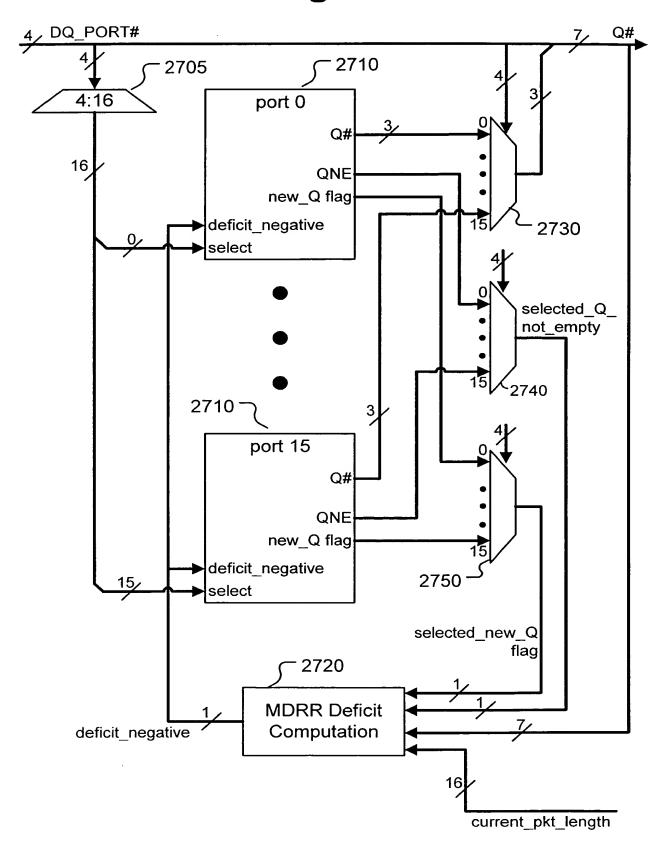
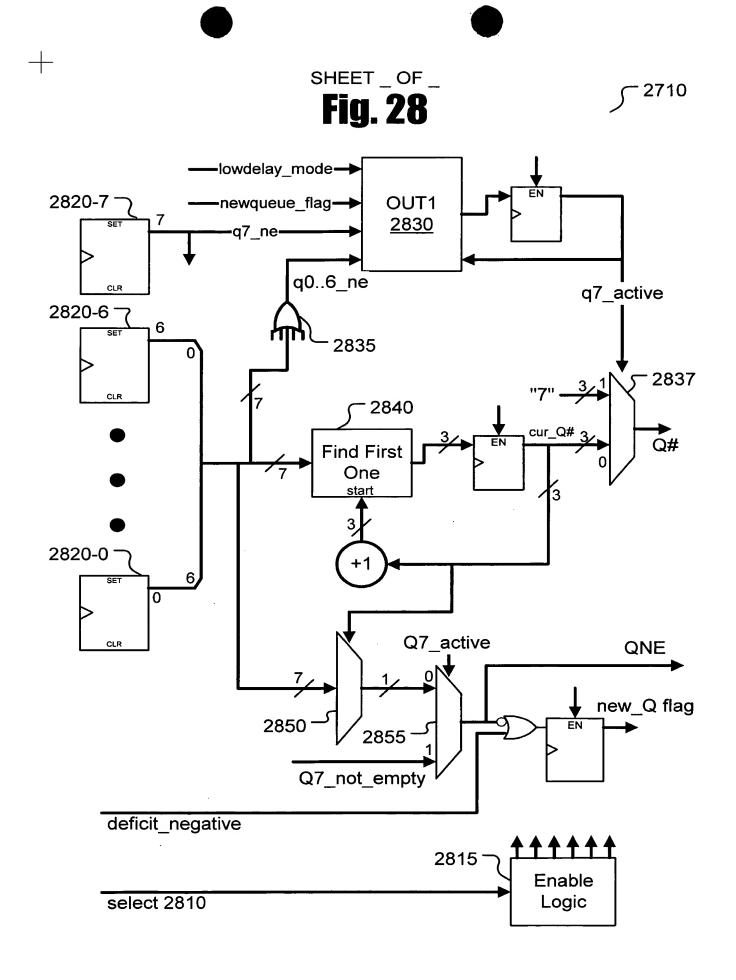
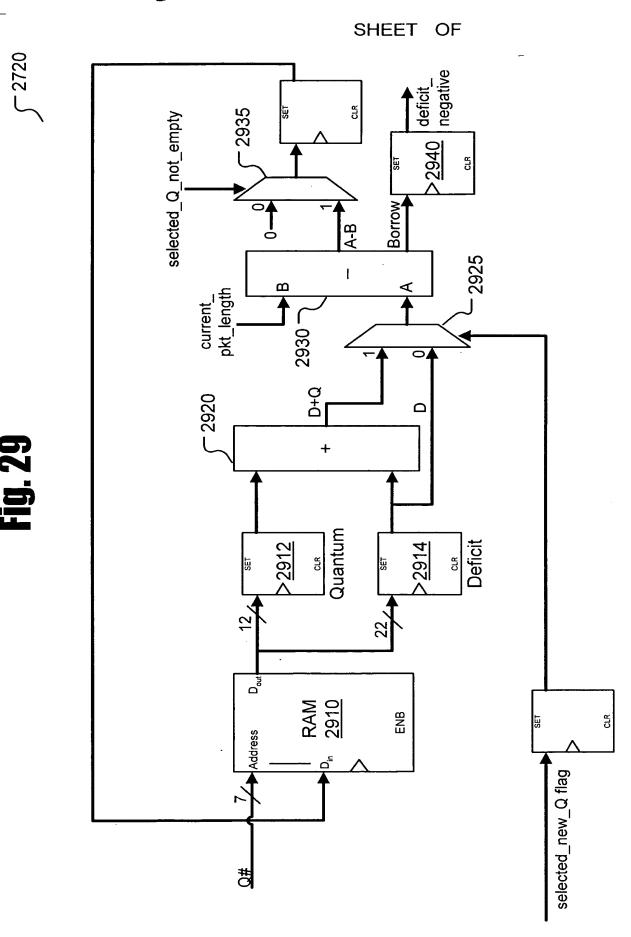


Fig. 27

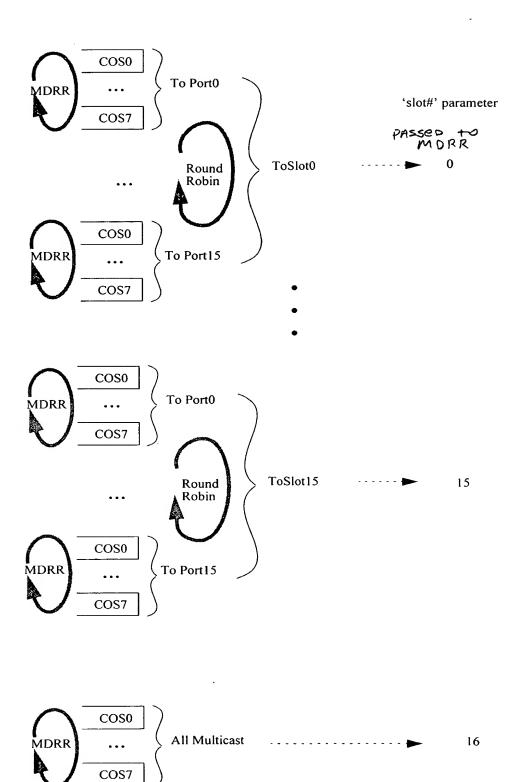


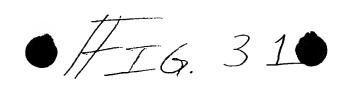


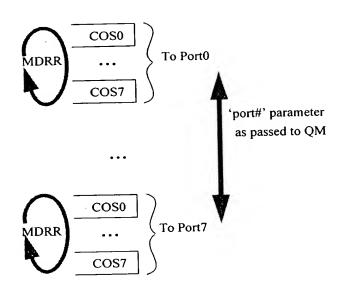












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